

Backplane wiring modifications for RC4000 DRC 401 for use with DDHF RC4000 Future Backing Storage™

A		B				Length 1)	Signal name	Note
Pos.	Pin	Pos.	Pin	Testp.	Diagram page			
47	1	61	35	H	35	35,4	IO BUS(5)	
47	2	61	36	J	35	35,4	IO BUS(6)	
47	3	61	41	K	35	36,2	IO BUS(7)	
47	4	60	3	A	35	30,3	IO BUS(8)	
47	5	60	8	B	35	30,9	IO BUS(9)	
47	6	60	9	C	35	30,9	IO BUS(10)	
47	7	60	14	D	35	31,5	IO BUS(11)	
47	8	60	15	E	35	31,5	IO BUS(12)	
47	9	60	29	F	35	33,7	IO BUS(13)	
47	10	59	3	A	35	30,8	IO BUS(14)	
47	11	59	8	B	35	31,3	IO BUS(15)	
47	12	59	9	C	35	31,3	IO BUS(16)	
47	13	59	14	D	36	31,8	IO BUS(17)	
47	14	59	15	E	36	31,8	IO BUS(18)	
47	15	59	29	F	36	33,8	IO BUS(19)	
47	16	59	30	G	36	33,8	IO BUS(20)	
47	17	59	35	H	36	34,5	IO BUS(21)	
47	18	59	36	J	36	34,5	IO BUS(22)	
47	19	59	41	K	36	35,2	IO BUS(23)	
47	20	43	9	C	1	13,7	-,Gi First Segment	
47							Gnd	2
47							+5V	3
47	23	36	6	B	13	24,1	CpDSA(12:23)	
47	24	68	1	A	6	17,2	WE	
47	25	19	5		18	25,5	SP(0)	
47	26	4	29	H	6	35,3	NRZ Data From Clock Track	
47	27	4	24	G	6	36,0	-,Read Strobe From Clock Track	
47	28	4	35	K	6	34,8	NRZ Data From Data Track	
47	29	7	31		4	31,4	Conn	4
27		27				842,5		

Note	1	Necessary wire length (cm). Includes length for stripping and compensation for the average thickness of the existing layer of wires
	2	Prewired to pin 21
	3	Prewired to pin 22
	4	Originally unused input tied to logical 1 by a common pull up resistor by the following wiring: 7-34 -- 7-31 -- 7-27 -- 7-23 -- 7-26 (7-23 being 1K5 to +5V) Rewiring will connect 7-34 to 7-27 freing up 7-31 for connection to 47-29 and should use 2 extra Elco pins for 7-34 and 7-27 in order to establish wiring in a deviant color

Materials used:

56	gold plated Elco pins for crimp
8,42 m	thin tinned wire in color(s) different from: white/gray, red or blue